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Attorney Docket: 3183-61

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : LIN, et al.
Application No. : 10/721,288
Filed : November 26, 2003
Title : INTEGRATED CIRCUIT PACKAGING FOR
IMPROVING EFFECTIVE CHIP-BONDING AREA
Group Art Unit : 2812
Examiner : Unknown
Docket No. : 3183-61

Honorable Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL COVER SHEET

Sir:

Transmitted herewith for filing are the following:

1. STATUS INQUIRY.

The Commissioner is hereby authorized to charge any fees which may be required for the filing of this request to **Deposit Account No. 501874.**

Respectfully submitted,

October 6, 2004

By:

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STATUS INQUIRY

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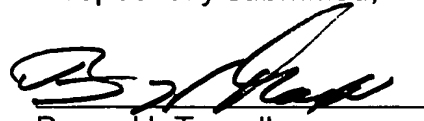
The above-identified U.S. patent application was filed in the U.S. Patent and Trademark Office on November 26, 2003 and a Filing Receipt was received by our office on February 27, 2004. As of this date, no Official Action has been received by Applicant.

Applicant therefore respectfully requests information regarding the status of the instant application.

Respectfully submitted,

Date: October 6, 2004

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